Free University of Bolzano–Principles of Compilers. Lecture IX, 2003/2004 – A.Artale

Principle of Compilers Lecture IX: Principles of Code Optimization

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2003/2004 - Second Semester

Summary of Lecture IX

- Code Optimization
- Basic Blocks and Flow Graphs
- Sources of Optimization
 - 1. Common Subexpression Elimination
 - 2. Copy Propagation
 - 3. Dead-Code Elimination
 - 4. Constant Folding
 - 5. Loop Optimization

Code Optimization: Intro

- Intermediate Code undergoes various transformations—called **Optimiza**tions—to make the resulting code running faster and taking less space.
- Optimization *never* guarantees that the resulting code is the best possible.
- We will consider only *Machine-Independent Optimizations*—i.e., they don't take into consideration any properties of the target machine.
- The techniques used are a combination of *Control-Flow* and *Data-Flow* analysis.
 - *Control-Flow Analysis*. Identifies loops in the flow graph of a program since such loops are usually good candidates for improvement.
 - Data-Flow Analysis. Collects information about the way variables are used in a program.

Criteria for Code-Improving Transformations

- The best transformations are those that yield the most benefit for the least effort.
 - 1. A transformation must preserve the meaning of a program. It's better to miss an opportunity to apply a transformation rather than risk changing what the program does.
 - 2. A transformation must, on the average, speed up a program by a measurable amount.
 - 3. Avoid code-optimization for programs that run occasionally or during debugging.
 - 4. **Remember!** Dramatic improvements are usually obtained by improving the source code: The programmer is always responsible in finding the best possible data structures and algorithms for solving a problem.

Free University of Bolzano–Principles of Compilers. Lecture IX, 2003/2004 – A.Artale Quicksort: An Example Program

• We will use the sorting program *Quicksort* to illustrate the effects of the various optimization techniques.

```
void quicksort(m,n)
int m,n;
{
    int i, j, v, x;
    if (n <= m) return;</pre>
    i = m-1; j = n; v = a[n]; /* fragment begins here */
    while (1) {
           do i = i+1; while (a[i] < v);
           do j = j-1; while (a[j]>v);
           if (i>=j) break;
           x = a[i]; a[i] = a[j]; a[j] = x;
    }
    x = a[i]; a[i] = a[n]; a[n] = x; /* fragment ends here */
    quicksort(m,j); quicksort(i+1,n);
```

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• The following is the three-address code for a fragment of Quicksort.

| (1) | i | := | m-1 | | | | (16) | t7 | 0 cm | 4*i |
|------|----------------|----------------|-------------|--------|------|--|------|-----------------|------|-----------------|
| (2) | j | : = | n | | | | (17) | t ₈ | * = | 4*j |
| (3) | t ₁ | := | 4 *n | | | | (18) | t9 | • | $a[t_8]$ |
| (4) | v | := | $a[t_1]$ | | | | (19) | $a[t_7]$ | := | t ₉ |
| (5) | i | := | i+1 | | | | (20) | t10 | := | 4*j |
| (6) | t_2 | := | 4*i | | | | (21) | $a[t_{10}]$ | := | x |
| (7) | t ₃ | := | $a[t_2]$ | | | | (22) | go | oto | (5) |
| (8) | if | t ₃ | < v go | oto (. | 5) | | (23) | t11 | := | 4*i |
| (9) | j | := | j-1 | | | | (24) | ж | : := | $a[t_{11}]$ |
| (10) | t_4 | := | 4*j | | | | (25) | t ₁₂ | := | 4*i |
| (11) | t ₅ | := | $a[t_4]$ | | | | (26) | t13 | := | 4*n |
| (12) | if | t ₅ | > v go | oto (| 9) | | (27) | t14 | := | $a[t_{13}]$ |
| (13) | if | i | >= j g | oto (| (23) | | (28) | a[t12] | := | t ₁₄ |
| (14) | t_6 | := | 4*i | | | | (29) | t | ; := | 4*n |
| (15) | x | := | $a[t_6]$ | | | | (30) | a[t15] | := | x |
| | | | - 0 - | | | | | | | |

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Basic Blocks and Flow Graphs

- The Machine-Independent Code-Optimization phase consists of control-flow and data-flow analysis followed by the application of transformations.
- During Control-Flow analysis, a program is represented as a *Flow Graph* where:
 - Nodes represent *Basic Blocks*: Sequence of consecutive statements in which flow-of-control enters at the beginning and leaves at the end without halt or branches;
 - Edges represent the flow of control.

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• Flow graph for the three-address code fragment for quicksort. Each B_i is a basic block.



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The Principal Sources of Optimization

- We distinguish *local* transformations—involving only statements in a single basic block—from *global* transformations.
- A basic block computes a set of expressions: A number of transformations can be applied to a basic block without changing the expressions computed by the block.
 - 1. Common Subexpressions elimination;
 - 2. Copy Propagation;
 - 3. Dead-Code elimination;
 - 4. Constant Folding.

Free University of Bolzano–Principles of Compilers. Lecture IX, 2003/2004 – A.Artale Common Subexpressions Elimination

- Frequently a program will include calculations of the same value.
- An occurrence of an expression E is called a *common subexpression* if E was previously computed, and the values of variables in E have no changed since the previous computation.
- Example. Consider the basic block B_5 . The assignments to both t_7 and t_{10} have common subexpressions and can be eliminated.

After local common subexpression elimination, B_5 is transformed as:

$$t_6 := 4 * i$$

 $x := a[t_6]$
 $t_8 := 4 * j$
 $t_9 := a[t_8]$
 $a[t_6] := t_9$
 $a[t_8] := x$
goto B_2

Common Subexpressions Elimination (Cont.)

- Example (Cont.) After local elimination, B_5 still evaluates 4 * i and 4 * j which are common subexpressions.
- 4 * j is evaluated in B_3 by t_4 . Then, the statements

 $t_8 := 4 * j; t_9 := a[t_8]; a[t_8] := x$

can be replaced by

$$t_9 := a[t_4]; a[t_4] := x$$

Now, a[t₄] is also a common subexpression, computed in B₃ by t₅. Then, the statements

 $t_9 := a[t_4]; a[t_6] := t_9$

can be replaced by

$$a[t_6] := t_5.$$

• Analogously, the value of x is the same as the value assigned to t_3 in block B_3 ; while t_6 can be eliminated and replaced by t_2 .

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• Example. The following flow graph shows the result of eliminating both local and global common subexpressions from basic blocks B_5 and B_6 .



DAGs for Determining Common Subexpressions

- To individuate common subexpressions we represent a basic block as a DAG showing how expressions are re-used in a block.
- A DAG for a Basic Block has the following labels and nodes:
 - 1. Leaves contain unique identifiers, either variable names or constants.
 - 2. Interior nodes contain an operator symbol.
 - 3. Nodes can optionally be associated to a list of variables representing those variables having the value computed at the node.

DAGs for Blocks: An Example

• The following shows both a three-address code of a basic block and its associated DAG.



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Copy Propagation

- Copy Propagation Rule: Given the *copy statement* x := y use y for x whenever possible after the copy statement.
- Copy Propagation applied to Block B_5 yields:

```
\begin{aligned} x &:= t_3 \\ a[t_2] &:= t_5 \\ a[t_4] &:= t_3 \\ \text{goto} B_2 \end{aligned}
```

This transformation together with Dead-Code Elimination (see next slide)
 will give us the opportunity to eliminate the assignment x := t₃ altogether.

Dead-Code Elimination

- A variable is *live* at a point in a program if its value can be used subsequently, otherwise it is *dead*.
- A piece of code is *dead* if data computed is never used elsewhere.
- Dead-Code may appear as the result of previous transformation.
- Dead-Code works well together with Copy Propagation.
- Example. Considering the Block B₅ after Copy Propagation we can see that x is never reused all over the code. Thus, x is a dead variable and we can eliminate the assignment x := t₃ from B₅.

Constant Folding

- Based on deducing at compile-time that the value of an expression (and in particular of a variable) is a constant.
- *Constant Folding* is the transformation that substitutes an expression with a constant.
- Constant Folding is useful to discover Dead-Code.
- Example. Consider the conditional statement: if (x) goto L.
 If, by Constant Folding, we discover that x is always false we can eliminate both the if-test and the jump to L.

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Loop Optimization

- The running time of a program can be improved if we decrease the amount of instructions in an inner loop.
- Three techniques are useful:
 - 1. Code Motion
 - 2. Reduction in Strength
 - 3. Induction-Variable elimination

Code Motion

- If the computation of an expression is *loop-invariant* this transformation places such computation before the loop.
- Example. Consider the following while statement: while (i <= limit - 2) do

The expression limit - 2 is loop invariant. Code motion transformation will result in:

t := limit -2; while (i \leq = t) do

Reduction in Strength

- Is based on the replacement of a computation with a less expensive one.
- Example. Consider the assignment t₄ := 4 * j in Block B₃.
 j is decremented by 1 each time, then t₄ := 4 * j 4.
 Thus, we may replace t₄ := 4 * j by t₄ := t₄ 4.
 Problem: We need to initialize t₄ to t₄ := 4 * j before entering the Block B₃.
 - *Result*. The substitution of a multiplication by a subtraction will speed up the resulting code.

Induction Variables

- A variable x is an **Induction Variable** of a loop if every time the variable x changes values, it is incremented or decremented by some constant.
- A common situation is one in which an induction variable, say i, indexes an array, and some other induction variable, say t, is the actual offset to access the array:
 - Often we can get rid of i.
 - In general, when there are two or more Induction Variables it is possible to get rid of all but one.

Induction Variables Elimination: An Example

- Example. Consider the loop of Bock B_3 . The variables j and t₄ are Induction Variables. The same applies for variables i and t₂ in Block B_2 .
- After Reduction in Strength is applied to both t₂ and t₄, the only use of i and j is to determine the test in B₄.
- Since $t_2 := 4 * i$ and $t_4 := 4 * j$ the test $t_2 > t_4$ is equivalent to i > j.
- After this replacement in the test, both i (in Block B_2) and j (in Block B_3) become dead-variables and can be eliminated! (see next slide for the new optimized code).

Free University of Bolzano–Principles of Compilers. Lecture IX, 2003/2004 – A.Artale Induction Variables Elimination: An Example (Cont.)

• Flow Graph after Reduction in Strength and Induction-Variables elimination.



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